Flexible, Configurable, HW-Accelerated: how an optimized SW/HW architecture can overcome gateway limitations

Tobias Islinger, Infineon Technologies AG Illia Safiulin, Elektrobit Roman Pallierer, Elektrobit

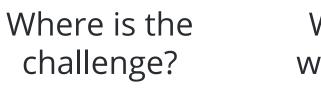
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Agenda

01



Where are we standing now?

02

What can the future architecture look like?

03

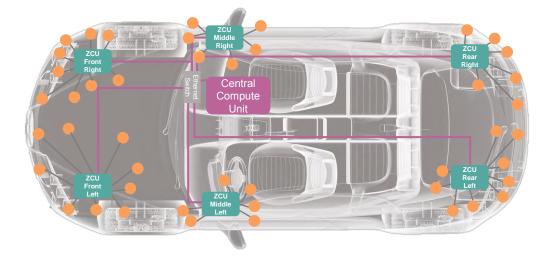
Conclusion

04

Where is the challenge?

Zonal Architecture increases the consolidation of features & ECUs





New E/E architecture concepts consolidate features and reduce the number of ECUs **Challenge:** Offer more features while reducing number of ECUs



By reducing the number of ECUs, data routing is getting more important and at the same time more challenging





Domain Architecture

- Routing time: 1ms
- Traffic < 1000 fps</p>
- PDU Routing (no HW acceleration)



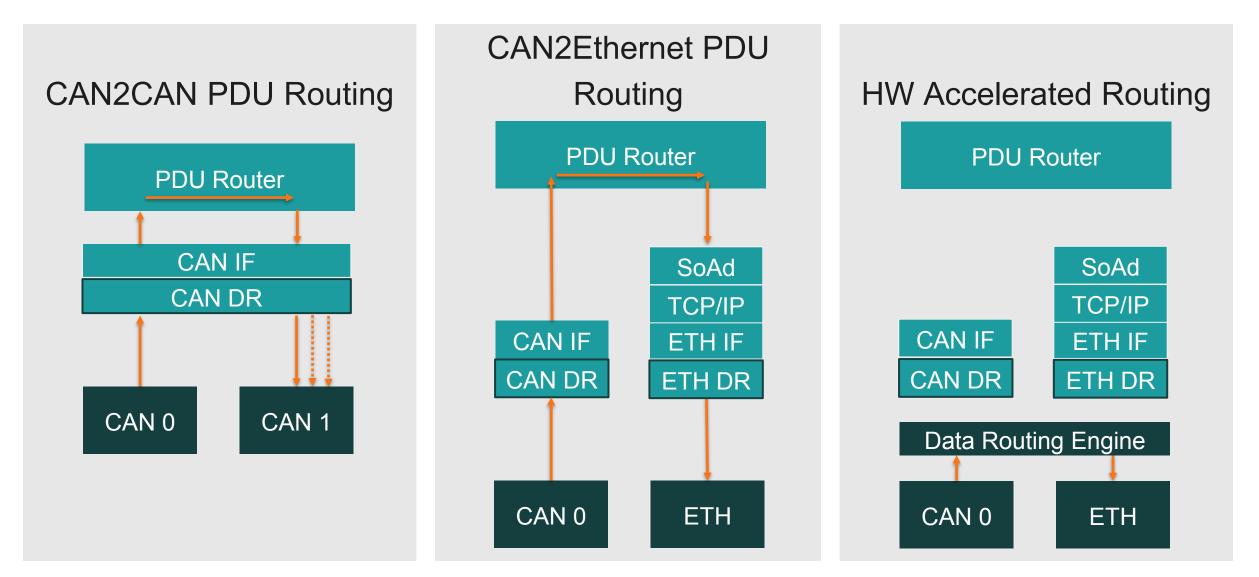
Zonal Architecture

- Routing time: 1ms
- Traffic >> 1000 fps
- PDU Routing (no HW acceleration)

Clips from the movie "Modern Times" by Charlie Chaplin

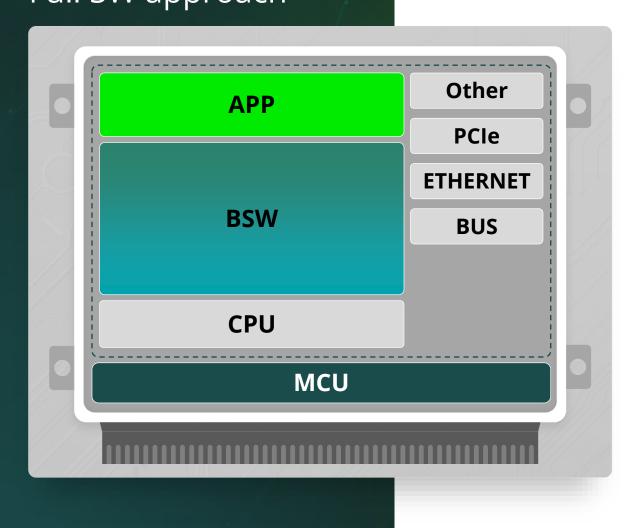
Data Routing can be achieved in different ways





Where are we standing now?

Performance Full SW approach



Fully executed by CPU

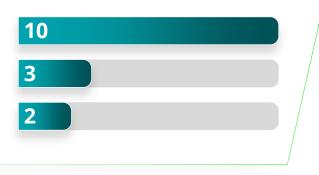
Pros:

- Highest flexibility and reusability
- Easy to deploy new features

Cons:

- Performance will not be sufficient
- Cost efficiency is questionable

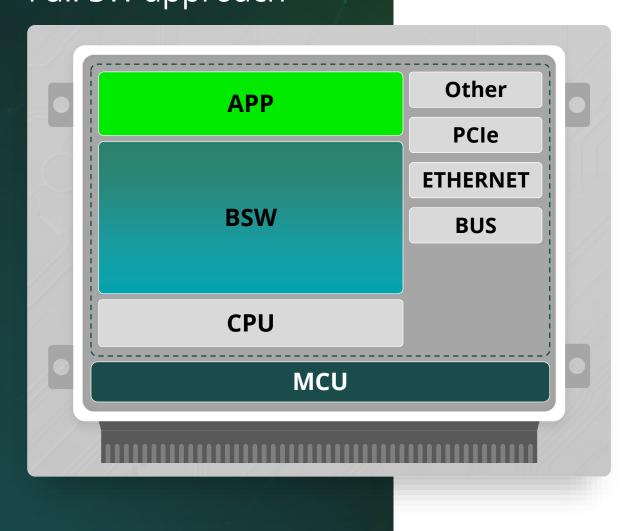
Flexibility Performance Power Consumption



Comparing the performance of different routing options SW based (PDU) routing only



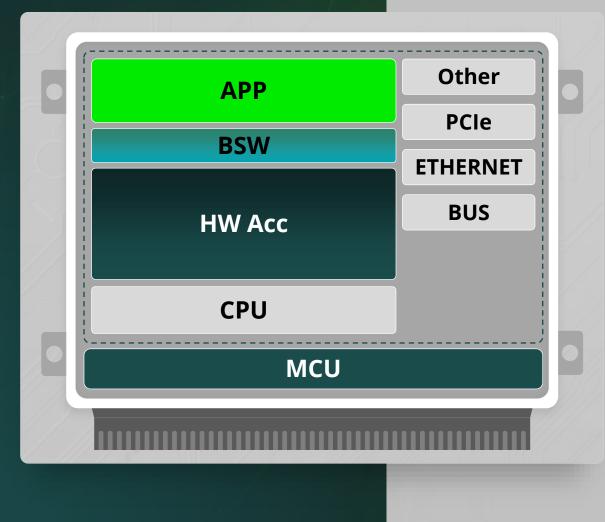
Performance Full SW approach



Fully executed by CPU

Performance

Full HW approach



Fully executed by HW Acc

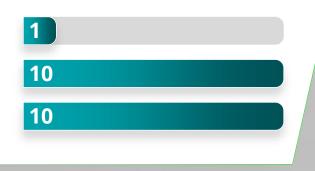
Pros:

- Highest
 performance
- Lowest power consumption

Cons:

- Not possible to implement new features
- Limited configuration space or complex configuration process

Flexibility Performance Power Consumption

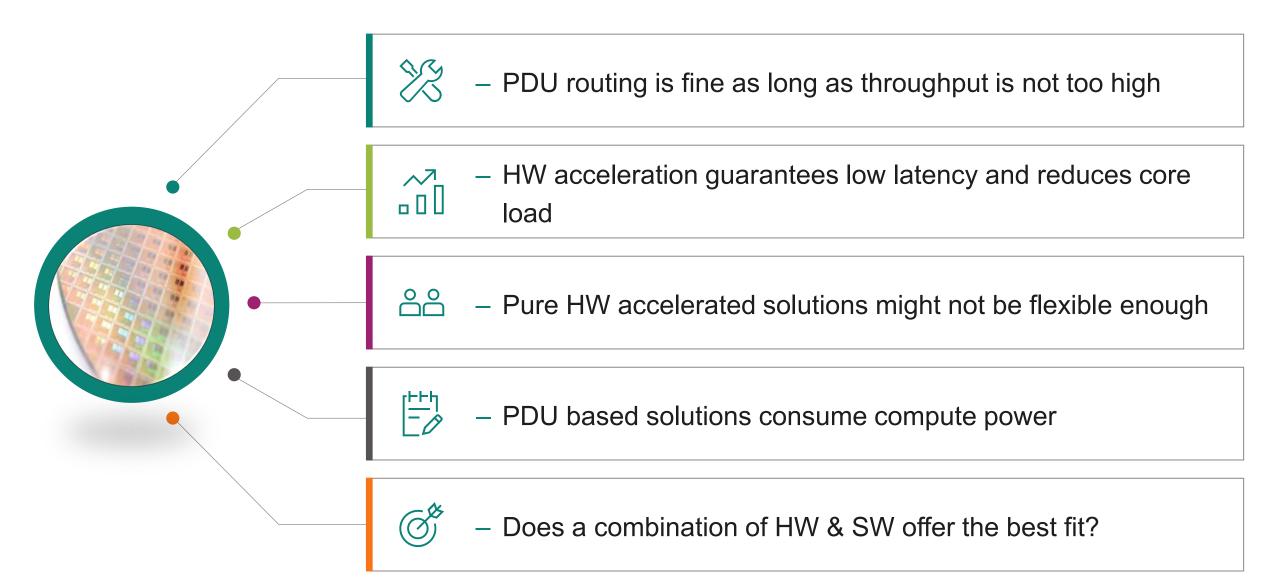


Comparing the performance of different routing options SW based (PDU) routing vs HW accelerated routing



Key takeaways of the different routing options





What can the future architecture look like?

Outlook Full HW approach

	Other
APP	Other
	PCle
BSW	ETHERNET
HW Acc	BUS
CPU	
MCU	

Fully executed by HW Acc

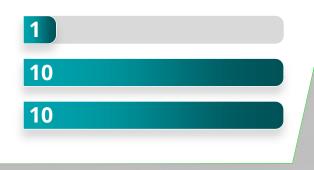
Pros:

- Highest
 performance
- Lowest power consumption

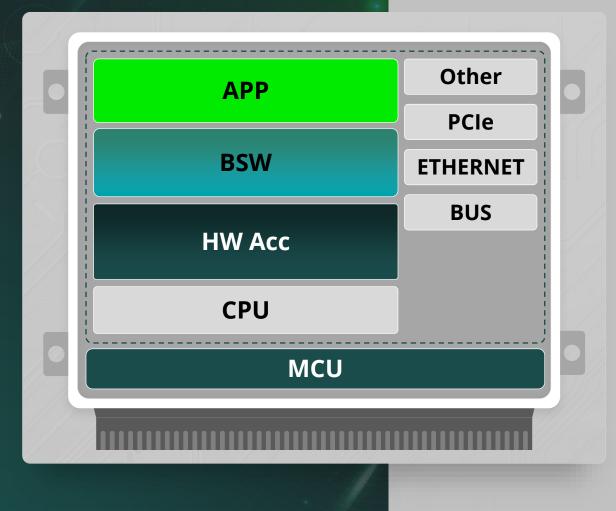
Cons:

- Not possible to implement new features
- Limited configuration space or complex configuration process

Flexibility Performance Power Consumption



Outlook Combined approach



Executed by <u>combination</u>

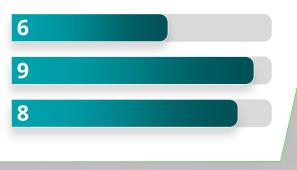
Pros:

- The most balanced approach
- Performance and flexibility is preserved

Cons:

- Currently many open points
- What to do with standardization?

Flexibility Performance Power Consumption



Comparing the performance of different routing options SW based (PDU) routing vs HW accelerated routing vs. Combined

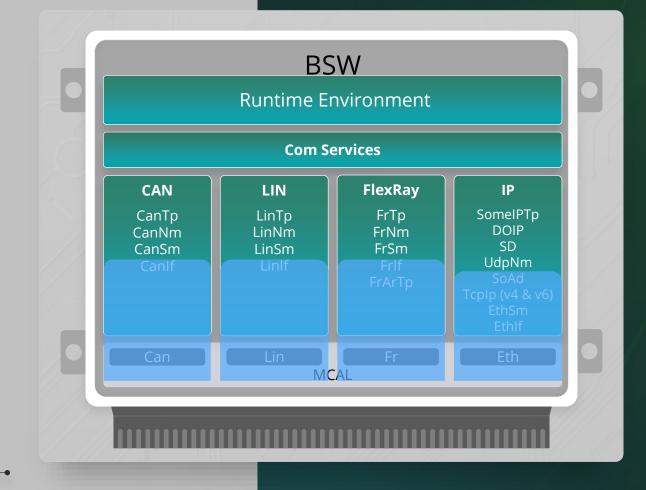


Outlook

Combined approach

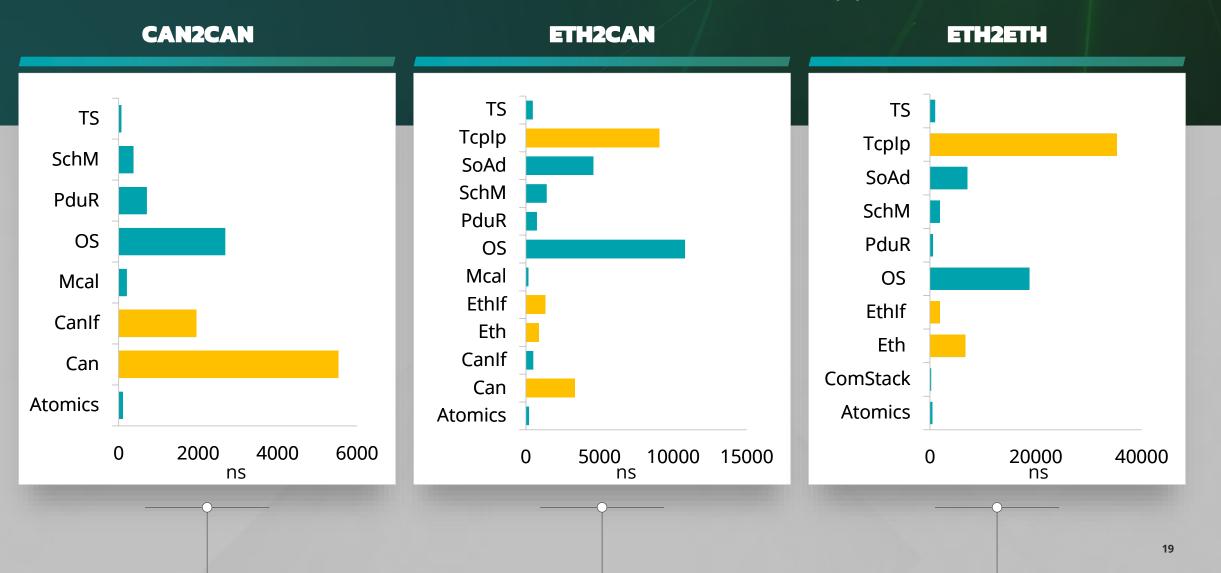
Open points with proposed architecture/solution

- Where is the boundary between Classic AUTOSAR and HW acceleration to be defined?
- Smooth integration of HW-related configuration parameters:
 - Special configuration model?
 - Common interfaces?
- How to support OEM-specific handling of information?



Where is the border between HW and SW?

Let's have a look where are the bottlenecks in full SW approach



Outlook

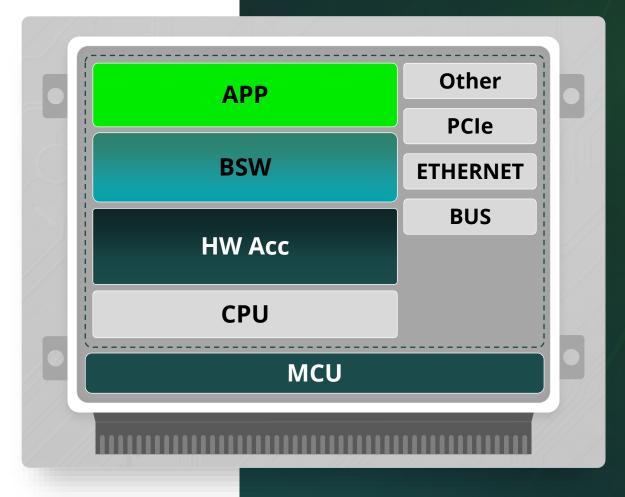
Combined approach

Smooth handling of HW-related configuration parameters:

Abstraction model to be considered in configuration

How to support OEM-specific handling of information?

Closer collaboration with OEMs and T1s is needed to solve common challenges





Conclusions

- Existing architectures can be served with the support of modern accelerator modules in MCUs and SoCs.
- However, future E/E architectures require even more advanced HW and SW solution.
- Investigation in new flexible and performant solutions has already started.
- Authors propose to have an early collaboration work with all stakeholders to find optimized solution.



Contact us



Dr. Tobias Islinger

Illia Safiulin

Dr. Roman Pallierer

System Architect for communication technologies

Infineon Technologies AG Tobias.Islinger@infineon.com Product Manager EB zoneo

Elektrobit Illia.Safiulin@elektrobit.com Head of Product Management Automotive Networks

Elektrobit Roman.Pallierer@elektrobit.com





