

**2022 IEEE Standards Association (IEEE SA)
Ethernet & IP @ Automotive Technology Day
9-10 November 2022
PACIFICO NORTH (4TH FLOOR)
Yokohama, Japan**

**PRESENTATIONS AND EXHIBITION
PROGRAM**

JAPAN STANDARD TIME ZONE

Last Updated: 7 November 2022

(SUBJECT TO CHANGE WITHOUT NOTICE)

DAY 1: WEDNESDAY, 9 NOVEMBER 2022

<p>9:00 -9:30 JST</p>	<p>Opening: Master of Ceremonies <i>Rudi Schubert (IEEE Standards Association)</i> Welcome Speech: Takashi Yoshizawa <i>(Vice President, Nissan Motor)</i> <i>(Representative Director, JASPAR)</i></p>
<p align="center">YANG, SDN</p>	
<p>9:30 - 10:00 JST</p>	<p>Proposal of Dynamically Configurable In-Vehicle Network as an Enabler of Software Defined Vehicle <i>Takumi Nomura (Honda, JASPAR) - Speaker</i> <i>Katsuyuki Akizuki (NEC Communication Systems, JASPAR) - Speaker</i> <i>Hideki Goto (Toyota, JASPAR)</i> <i>Yoshihiro Ito (Nagoya Institute of Technology, JASPAR)</i></p>
<p>10:00 – 10:30 JST</p>	<p>Ethernet as a Service for Software Defined Vehicles: Design Objectives and Orientations <i>Pierre Laclau (Stellantis) - Speaker</i> <i>Xiaoting Li (Stellantis)</i> <i>Trista Lin (Stellantis)</i></p>
<p>10:30 – 11:00 JST</p>	<p>COFFEE BREAK</p>
<p align="center">APPLICATION, HIGHER LAYER CONNECTIVITY</p>	
<p>11:00 -11:30 JST</p>	<p>Centralized Software and Zonal Architectures for Future Innovative Ambient Lighting Enabled by E²B 10BASE-T1S <i>Fionn Hurley (Analog Devices) - Speaker</i> <i>Peter Wilms (BMW)</i></p>
<p>11:30 – 12:00 JST</p>	<p>TCP and Automotive Ethernet – How to Resolve the Everlasting Struggle <i>Iago Alvarez (Technica Engineering) - Speaker</i> <i>Karl Budweiser (BMW)</i> <i>Stefan Lachner,</i> <i>Lars Völker (Technica Engineering)</i></p>
<p>12:00-12:30 JST</p>	<p>New Automotive gPTP Application and Their Performance Needs <i>Don Pannell (NXP) - Speaker</i> <i>Rajeev Roy (NXP) – Speaker</i></p>
<p>12:30 - 14:00 JST</p>	<p>LUNCH PLEASE VISIT THE EXHIBITION ROOM G403+G404</p>

SAFETY & SECURITY	
14:00 – 14:30 JST	Security Protocols and Applications – Best Friends or Worst Enemies <i>Antonio Gallego (Technica Engineering)- Speaker</i> <i>Jose Galve (Technica Engineering)</i> <i>Lars Völker (Technica Engineering)</i>
14:30 – 15:00 JST	From TC10 to System Wake-Up – Safety System Solutions <i>Steffen Lorenz (NXP Semiconductors)- Speaker</i>
15:00 – 15:30 JST	COFFEE BREAK
VALIDATION AND TEST	
15:30 – 16:00 JST	PHY Latency and its Effects on TSN Performance <i>Martin Gubow (Keysight Technologies) - Speaker</i> <i>Alon Regev (Keysight Technologies) - Speaker</i>
16:00 – 16:30 JST	PANEL SET UP
16:30- 18:00 JST	PANEL SESSION Moderators: <i>Professor Osami Wada (Kyoto University)</i> <i>Associate Professor Yoshihiro Ito (Nagoya Institute of Technology)</i> Panelists: <i>Amir Bar-Niv (Marvell)</i> <i>Christoph Gollob (BMW)</i> <i>Martin Gubow (Keysight)</i> <i>Chris Mash (Ethernovia)</i> <i>Illia Safiulin (Elektrobit)</i> <i>Naoshi Serizawa (YAZAKI)</i>

<p>NETWORKING DINNER 18:00 – 20:00 JST ANNEX HALL SECOND FLOOR ROOM: F201-F202 3-MINUTE WALK TO ANNEX HALL FROM PACIFICO NORTH THROUGH THE PEDESTRIAN DECK MEET OUTSIDE G401+G402 AT 18:00 JST TO WALK TO ANNEX HALL</p>
<p>VISIT THE EXHIBITION ROOM G403+G404 9:00 - 17:00 JST</p>

DAY 2: THURSDAY 10 NOVEMBER 2022

NETWORK AND ARCHITECTURE

9:00 - 9:30 JST	Automotive SDN Prototype and Use Cases <i>Hyun Taek Hong (LG Electronics Inc.)- Speaker</i> <i>Kilho Lee (Soongsil University)- Speaker</i>
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SERVICE ORIENTED ARCHITECTURE

9:30 – 10:00 JST	Signal to Service: Remote Control of Legacy Networks <i>Brian Petersen (Ethernovia) - Speaker</i>
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TSN

10:00 – 10:30 JST	Real Life Performance of IEEE 1722 Control Format (ACF) <i>Pablo Granados (CETITEC) - Speaker</i> <i>Joe Nguyen (CETITEC)</i> <i>Sergei Shoev (CETITEC)</i>
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10:30 – 11:00 JST	COFFEE BREAK
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11:00 – 11:30 JST	Layer 2 Security for Precision Time Protocol Frames for Automotive Ethernet <i>Shubham Agarwal (Cadence Design System) - Speaker</i> <i>Krunalkumar Patel (Cadence Design System)- Speaker</i>
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11:30 – 12:00 JST	Network-Scale Virtual Output Queues <i>Brian Petersen (Ethernovia) – Speaker</i>
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12:00 – 12:30 JST	A Study of Ethernet TSN Profile Based on JASPAR’s Automotive Use Cases <i>Tatsuya Izumi (Sumitomo Electric, JASPAR) - Speaker</i> <i>Yoshihiro Ito (Nagoya Institute of Technology, JASPAR) - Speaker</i> <i>Takeshi Tajima (Aubass, JASPAR)</i> <i>Hideki Goto (Toyota, JASPAR)</i> <i>Yasuhiro Yamasaki (Toyota, JASPAR)</i> <i>Takumi Nomura (Honda, JASPAR)</i> <i>Yasuhiro Kotani (DENSO, JASPAR)</i> <i>Katsuyuki Akizuki (NEC Communication Systems, JASPAR)</i>
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12:30 - 14:00 JST	LUNCH PLEASE VISIT THE EXHIBITION ROOM G403+G404
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PHY, switch, μC	
14:00 – 14:30 JST	Optimized Ethernet Switch HWSW Collaboration for New EE Architectures <i>Illia Safiulin (Elektrobit)- Speaker</i>
14:30 – 15:00 JST	Concept of Test Points in In-Vehicle Optical Physical Layer Standardization for Multi-Vendorization <i>Keisuke Kawahara (Furukawa Electric, JASPAR) - Speaker</i> <i>Naoshi Serizawa (Yazaki, JASPAR) - Speaker</i> <i>Hideki Goto (Toyota, JASPAR)</i> <i>Manabu Kagami (Nagoya Institute of Technology, JASPAR)</i> <i>Takumi Nomura (Honda, JASPAR)</i> <i>Masato Shiino (Furukawa Electric, JASPAR)</i> <i>Hiromasa Tanaka (Japan Aviation Electronics Industry, JASPAR)</i>
15:00 – 15:30 JST	COFFEE BREAK
15:30 – 16:00 JST	Enhancing Automotive Ethernet Efficiency for Emerging, Asymmetrical Use Cases <i>Amir Bar-Niv (Marvell Technology) - Speaker</i> <i>Thomas Hogenmüller (Robert Bosch) - Speaker</i>
16:00 – 16:30 JST	Topology Discovery Feature for 10BASE-T1S Multi-Drop <i>Piergiorgio Beruto (Onsemi) - Speaker</i>
16:30 JST	Closing Remarks Master of Ceremonies <i>Rudi Schubert (IEEE Standards Association)</i>
VISIT THE EXHIBITION ROOM G403+G404 9:00 - 15:30 JST	
CATEGORIES	
NETWORK AND ARCHITECTURE	SERVICE-ORIENTED ARCHITECTURE
APPLICATION, HIGHER LAYER CONNECTIVITY	SAFETY & SECURITY
TSN	VALIDATION AND TEST
YANG, SDN	PHY, switch, μC