Errata to
IEEE Standard for Error Correction Coding of Flash Memory Using Low-Density Parity Check Codes

Developed by the
Microprocessor Standards Committee
of the
IEEE Computer Society

Correction Sheet
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In subclause 1.1 and subclause 1.2, change “draft standard” to “standard” as follows:

1.1 Scope
This standard specifies a method to construct two-level low-density parity-check (LDPC) codes and to utilize them as the error correction coding (ECC) scheme in non-volatile memories (NVM).

1.2 Purpose
This standard provides a method to utilize error correction coding in non-volatile memories.