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23 August 2007

Leslie A Orlidge  
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Re: P1505.2 - Standard for the Common Test Interface Pin Map Configuration for Double-Tier Test Requirements Utilizing IEEE Std 1505

Dear Leslie:

I am pleased to inform you that on 22 August 2007 the IEEE-SA Standards Board approved the above referenced project until 31 December 2011. A copy of the file can be found on our website at <http://standards.ieee.org/board/nes/projects/1505-2.pdf>.

Now that your project has been approved, please forward a roster of participants involved in the development of this project. This request is in accordance with the IEEE-SA Operations Manual, Clause 5.1.2i under Duties of the Sponsor which states:

"Submit annually to the IEEE Standards Department an electronic roster of individuals participating on standards projects"

For your convenience, an Excel spreadsheet for your use has been posted on our website at <http://standards.ieee.org/guides/par/roster.xls>. Please forward this list to me via e-mail at [s.hampton@ieee.org](mailto:s.hampton@ieee.org) no later than 20 November 2007.

Please visit our website, IEEE Standards Development Online (<http://standards.ieee.org/resources/development/index.html>), for tools, forms and training to assist you in the standards development process. Also, we strongly recommend that a copy of your draft be sent to this office for review prior to the final vote by the working group to allow for a quick review by editorial staff before sponsor balloting begins.

If you should have any further questions, please contact me at +1 732 562 6003 or by email at [s.hampton@ieee.org](mailto:s.hampton@ieee.org).

Sincerely,

Sherry Hampton  
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Standards Activities  
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<b>PAR Request Date:</b> 25 May 2007	
<b>PAR Approval Date:</b> 22 August 2007	
<b>PAR Signature Page on File:</b> Yes	
<b>Type of PAR:</b> New IEEE Standard	
<b>Status:</b> PAR for a New IEEE Standard	
<b>Root Project:</b>	
<b>1.1 Project No.:</b> <b>1505.2</b>	
<b>1.2 Type of Document:</b> Standard	
<b>1.3 Life Cycle:</b> Full-Use	
<b>1.4 Is this document in ballot now?</b> No	
<b>2.1 Title</b> Standard for the Common Test Interface Pin Map Configuration for Double-Tier Test Requirements Utilizing IEEE Std 1505	
<b>3.1 Working Group Name</b>	<a href="#">Hardware Interfaces Working Group</a>
<b>Working Group Chair</b>	<a href="#">Stora, Michael</a> Phone: 973-299-8321 Email: mjs@sysintech.com
<b>Working Group Vice Chair</b>	<a href="#">Droste, David B</a> Phone: 256-895-2311 Email: ddroste@drs-tem.com
<b>3.2 Sponsor</b>	<a href="#">IEEE-SA Standards Board Test and Diagnosis for Electric Systems (SASB/SCC20)</a>
<b>Sponsor Chair</b>	<a href="#">Orlidge, Leslie A</a> Phone: 410-628-6634 Email: orlidge@aaicorp.com
<b>Name of Standards Liaison Representative (if applicable)</b>	
<b>3.3 Joint Sponsor</b>	<a href="#">IEEE Instrumentation and Measurement Society TC5 - Connectors in Measurements (IM/Con)</a>
<b>4.1 Type of Ballot:</b> Individual	
<b>4.2 Expected Date of Submission for Initial Sponsor Ballot:</b> December 2009	
<b>4.3 Projected Completion Date for Submittal to RevCom:</b> May 2010	
<b>5.1 Approximate number of people expected to work on this project:</b> 25	
<b>5.2 Scope:</b> The scope of this standard is the definition of a two-tier pin map utilizing IEEE 1505 Receiver-Fixture Interface (RFI). This addresses only the second tier that will supplement single (1st tier) of 1505.1 for systems that require assets not included in the 1st tier. The pin map defined within this standard shall apply to mid-size to large military and Aerospace Automatic Test Equipment (ATE) testing applications.	
<b>5.3 Is the completion of this document contingent upon the completion of another document?</b> Yes This project/standard is intended to supplement the IEEE-1505.1 Common Test Interface Standard planned for ballot in June 2007.	
<b>5.4 Purpose:</b> To provide a standardized common I/O Pin Map for the second tier of a two-tier IEEE-1505 Receiver-Fixture Interface to enable the interoperability of compliant interface fixtures (also known as interface test adapters, interface devices, or interconnection devices) on multiple ATE systems.	

**5.5 Need for the Project:** There is no current standard (formal or defacto) that defines a Two-Tier pin map for test systems utilizing IEEE-1505. Users of ATE Systems are unable to maximize their investment in ATE hardware because of the resultant incompatibility between systems. This standard will mitigate this issue.

**5.6 Stakeholders for the Standard:** Primary stakeholders are Military and Aerospace owners of large quantities of ATE Systems. Commercial organizations/users may benefit to a lesser extent in the standardization of ATE systems and interfaces.

**6.1.a. Has the IEEE-SA policy on intellectual property been presented to those responsible for preparing/submitting this PAR prior to the PAR submittal to the IEEE-SA Standards Board? Yes Presented Date: 2007-04-16**

If no, please explain:

**6.1.b. Is the Sponsor aware of any copyright permissions needed for this project? No**

If yes, please explain:

**6.1.c. Is the Sponsor aware of possible registration activity related to this project? No**

If yes, please explain:

**7.1 Are there other standards or projects with a similar scope? No**

If yes, please explain:

**Sponsor Organization:**

**Project/Standard Number:**

**Project/Standard Date:** 0000-00-00

**Project/Standard Title:**

**7.2 Is there potential for this standard (in part or in whole) to be adopted by another national, regional, or international organization? ? Do not know at this time**

**Technical Committee Name and Number:**

**Contact person:**

**Contact person Phone Number:**

**Contact person Email Address:**

**7.3 Will this project result in any health, safety, security, or environmental guidance that affects or applies to human health or safety? No**

**7.4 Additional Explanatory Notes:**

**8.1 Sponsor Information:**

Is the Scope of this project within the approved scope/definition of the Sponsor's Charter? Yes

If no, please explain: